



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Suresh Balasubramanian

Serial No.: 10/015,741

Filed: 12/17/01

For: Generating a Lock Signal Indicating Whether an Output Clock Signal Generated by a PLL is in Lock with an Input Reference Signal

TI-33469

Examiner: TBD

Art Unit: 2631

2631  
#3  
W. H. Lee  
7-17-02

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MAR 21 2002

LETTER TO THE OFFICIAL DRAFTSPERSON

Technology Center 2600

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on 2-26-02 as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C.

Karen Vertz  
Karen Vertz

2-26-02  
Date

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Enclosed are SIX (6) sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Respectfully submitted,

Rose Alyssa Keagy  
Attorney for Applicant  
Reg. No. 35,095

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